



PTO/SB/21 (08-00)

MODIFIED

**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

* Plus 6 cited references

Total Number of Pages in This Submission

5 *

Application Number 09/920,240

Filing Date 08/01/2001

First Named Inventor Pierre ROO

Group Art Unit 2682

Examiner Name Eugene Yun

Attorney Docket Number MP0039.CIP

ENCLOSURES (check all that apply)

- ☒ Fee Transmittal Form
☒ Credit Card Authorization
☐ Amendment / Reply
☐ After Final
☐ Affidavits/declaration(s)
☐ Extension of Time Request
☐ Express Abandonment Request
☐ Information Disclosure Statement with
☐ Certified Copy of Priority Document(s)
☐ Response to Missing Parts/ Incomplete Application
☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

- ☐ Assignment Papers (for an Application)
☐ Drawing(s)
☐ Licensing-related Papers
☐ Petition
☐ Petition to Convert to a Provisional Application
☐ Associate Power of Attorney
☐ Terminal Disclaimer
☐ Request for Refund
☐ CD, Number of CD(s) _____

- ☐ After Allowance Communication to Group
☐ Appeal Communication to Board of Appeals and Interferences
☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
☐ Proprietary Information
☐ Status Letter
☐ Other Enclosure(s) (please identify below):

Remarks

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENTFirm,
or
Individual nameAndrew J. Bateman
Registration No.: 45,573

Signature

Date

10/03/3005

CERTIFICATE OF MAILINGI hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:

Typed or printed name

Signature

Date

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /EY/



FEE TRANSMITTAL for FY 2005

Effective 10/01/2005, Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) **180.00**

Complete if Known

Application Number 09/920,240
 Filing Date 08/01/2001
 First Named Inventor Pierte ROO
 Examiner Name Eugene Yun
 Art Unit 2682
 Attorney Docket No. MP0039.CIP

METHOD OF PAYMENT (check all that apply)

☐ Check ☒ Credit card ☐ Money Order ☐ Other ☐ None

Deposit Account:

50-1710

Account Name

KATTEN MUCHIN ROSENMAN, LLP

The Director is authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☒ Credit any overpayments

☒ Charge above Deposit Account with any additional fees necessary UNDER 37 CFR 1.16 AND/OR 1.17 to maintain pendency of this application.

FEE CALCULATION

1. BASIC FILING FEE

Large Entity	Small Entity	Description	Fee Paid
Fee Code (\$)	Fee Code (\$)		
1001 300	2001 150	Utility Filing Fee	
1111 500	2111 250	Utility Search Fee	
1311 200	2311 100	Patent Examination Fee	
SUBTOTAL (1)			(\$) 0.00

2. EXTRA CLAIM FEES FOR UTILITY AND REISSUE

Total Claims		- 20** =		X		50.00	
Independent		- 3** =		X		200.00	
Multiple Dependent						360.00	

Large Entity	Small Entity	Description	Fee Paid
Fee Code (\$)	Fee Code (\$)		
1202 50	2202 25	Claims in excess of 20	
1201 200	2201 100	Independent claims in excess of 3	
1203 360	2203 180	Multiple dependent claim, if not paid	
1204 200	2204 100	** Reissue independent claims over original patent	
1205 50	2205 25	** Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)			(\$) 0.00

** or number previously paid, if greater; For Reissues, see above

3. ADDITIONAL FEES

Large Entity	Small Entity	Description	Fee Paid
Fee Code	Fee Code		
1051 130	2051 65	Surcharge - late filing fee or oath	
1052 50	2052 25	Surcharge - late provisional filing fee or cover sheet	
1053 130	1053 130	Non-English specification	
1812 2,520	1812 2,520	For filing a request for ex parte reexamination	
1804 920*	1804 920*	Requesting publication of SIR prior to Examiner action	
1805 1,840*	1805 1,840*	Requesting publication of SIR after Examiner action	
1251 120	2251 60	Extension for reply within first month	
1252 450	2252 225	Extension for reply within second month	
1253 1,020	2253 510	Extension for reply within third month	
1254 1,590	2254 795	Extension for reply within fourth month	
1255 2,160	2255 1,080	Extension for reply within fifth month	
1401 500	2401 250	Notice of Appeal	
1402 500	2402 250	Filing a brief in support of an appeal	
1403 1,000	2403 500	Request for oral hearing	
1451 1,510	1451 1,510	Petition to institute a public use proceeding	
1452 500	2452 250	Petition to revive - unavoidable (1.17(f))	
1453 1,500	2453 750	Petition to revive - unintentional (1.17(m))	
1501 1,400	2501 700	Utility issue fee (or reissue)	
1502 800	2502 400	Design issue fee	
1503 1,100	2503 550	Plant issue fee	
1460 130	1460 130	Petitions to the Director	
1807 50	1807 50	Processing fee - provisional app (1.17(g))	
1806 180	1806 180	Submission of Information Disclosure Stmt	180.00
8021 40	8021 40	Recording each patent assignment per property (times number of properties)	
1809 790	2809 395	Filing a submission after final rejection (1.29(a))	
1814 130	2814 65	Statutory Disclaimer	
1801 790	2801 395	Request for Continued Examination (RCE)	
1802 900	1802 900	Req for expedited examination - Design App	
Other fee (specify)			
SUBTOTAL (3)			(\$) 180.00

*Reduced by Basic Filing Fee Paid

SUBMITTED BY

Name: Andrew J. Bateman

Registration No.: 45,573

Telephone: (202) 625-3547

Signature:

Date: 10/03/2005

WARNING: As information on this form may become public, credit card information should not be included. Provide credit card information/authorization on PTO-2038.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Pierle ROO

Application No.: 09/920,240

Filed: August 1, 2001

For: ACTIVE RESISTIVE SUMMER FOR
A TRANSFORMER HYBRID

Examiner: Eugene YUN

Group Art Unit: 2682

Confirmation No.: 4035

Date: October 3, 2005

INFORMATION DISCLOSURE STATEMENTCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and the requirements of MPEP § 2001.06(c), and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449 and to the copies of the literature references submitted herewith.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initial copy of the PTO-1449 be returned indicating that such information was considered.

A Fee Transmittal and Credit Card Authorization are being filed concurrently herewith authorizing payment of the \$180.00 IDS fee.

Should the Examiner have any questions, Applicant's undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

10/04/2005 SZEWDIE1 00000135 09920240

01 FC:1806

180.00 DP

IP Docket
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Facsimile No.: (202) 298-7570
Customer No.: 28285Respectfully submitted,
KATTEN MUCHIN ROSENMAN, LLP

By:

Andrew J. Bateman
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FORM PTO 1449 MODIFIED U.S. PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. MP0039.CIP	APPLICATION NO. 09/920,240
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT Pierle ROO	
		FILING DATE 08/01/2001	GROUP 2682
DATE SUBMITTED TO USPTO: October 3, 2005			
OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.)			
*EXAMINER INITIALS			
	Dunning, Jim, "An All-Digital Phase-Locked Loop with 50-Cycle Lock Time Suitable for High-Performance Microprocessors," IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 30, No. 4, April 1995, pp. 412-422		
	Mancatis, John G., "FA 8.1: Low-Jitter and Process-Independent DLL and PLL Based on Self-Biased Techniques"		
	Moon, Yongsam, et al., "An All-Analog Multiphase Delay-Locked Loop Using a Replica Delay Line for Wide-Range Operation and Low-Jitter Performance," IEEE JOURNAL OF SOLID-STATE CIRCUITS, Vol. 35, No. 3, March 2000, pp. 377-384		
	Wang, HongMo, "WP 23.8: A 9.8GHz Back-Gate Tuned VCO in 0.35µm CMOS," ISSCC99, Session 29, Paper WP 23.8, 1999 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, pp. 400-407 & 484		
	Rudell, et al., "SA 18.3: A 1.9 GHz Wide-Bank IF Double Conversion CMOS Integrated Receiver for Cordless Telephone Applications," 1997, pp. 304-305, 476		
	Garlepp, Bruno W., et al., "18.3 A Portable Digital DLL Architecture for CMOS Interface Circuits," 1998 SYMPOSIUM ON VLSI CIRCUITS DIGEST OF TECHNICAL PAPERS," pp. 214-215		
EXAMINER	/Eugene Yun/		DATE CONSIDERED 02/20/2009

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.